

**IN THE SPECIFICATION:**

Please amend the following passage found in the specification at paragraph [0046] of the originally filed case.

--[0046] Another exemplary implementation of an integrated switch is now described in the context of a distributed-memory computer comprising 10 switches. Each switch contains 9 routers connected as a two-dimensional switch network. Such a network is an example of the network described in commonly owned, co-pending U.S. Patent Application No. 10/793,068 \_\_\_\_\_ (U.S. Attorney Docket No. YOR920030237US2 (16774)) entitled "A Multi-dimensional Switch Network," the contents and disclosure of which is incorporated by reference as if fully set forth herein. In such a network, each dimension of the network is three (3) routers in length. Each router is connected to one node. Each of the nine (9) routers in a switch has an external link to one of the other switches. Thus each of the 10 switch elements is linked to every other switch.--